

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE


[Membership](#) | [Publications/Services](#) | [Standards](#) | [Conferences](#) | [Careers/Jobs](#)
IEEE Xplore[®]
RELEASE 1.6

 Welcome
 United States Patent and Trademark Office


» Sea

[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)
[Quick Links](#)
Welcome to IEEE Xplore[®]

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **19** of **1022101** documents.
 A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or enter a new one in the text box.

☐ Check to search within this result set
Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Design and characterisation of a CMOS VLSI self-timed multiplier architecture based on a bit-level pipelined-array structure

Acosta, A.J.; Jimenez, R.; Barriga, A.; Bellido, M.J.; Valencia, M.; Huertas, J.L
 Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-Circuits, Devices and Systems] , Volume: 145 , Issue: 4 , Aug. 1998
 Pages:247 - 253

[\[Abstract\]](#) [\[PDF Full-Text \(656 KB\)\]](#) **IEE JNL**
2 Design, Automation and Test in Europe Conference and Exhibition, 1 Proceedings (Cat. No. PR00078)

Design, Automation and Test in Europe Conference and Exhibition 1999.
 Proceedings , 9-12 March 1999

[\[Abstract\]](#) [\[PDF Full-Text \(544 KB\)\]](#) **IEEE CNF**
3 The use of ISDN signaling for real-time applications at homes and businesses

Prakash, N.; Elkateeb, A.;
 Electrical and Computer Engineering, 1998. IEEE Canadian Conference on , Volume: 2 , 24-28 May 1998
 Pages:637 - 640 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) **IEEE CNF**
4 Frequency diversity gain in indoor GSM systems

Hook, M.; Johansson, C.; Olofsson, H.;
 Vehicular Technology Conference, 1996. 'Mobile Technology for the Human Ra
 IEEE 46th , Volume: 1 , 28 April-1 May 1996
 Pages:316 - 320 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(424 KB\)\]](#) IEEE CNF

5 Distributed optical ATM switching architectures

Bartos, R.; Kannan, R.; Lee, K.Y.; Jordan, H.F.;

Global Telecommunications Conference, 1995. GLOBECOM '95., IEEE , Volume 3 , 13-17 Nov. 1995

Pages:1751 - 1755 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(472 KB\)\]](#) IEEE CNF

6 Dedicated systolic VLSI circuit as adaptive filter for acoustic ech canceller

Roncella, R.; Saletti, R.; Terreni, P.;

CompEuro '91. 'Advanced Computer Technology, Reliable Systems and Applications'. 5th Annual European Computer Conference. Proceedings. , 13-1 May 1991

Pages:393 - 397

[\[Abstract\]](#) [\[PDF Full-Text \(276 KB\)\]](#) IEEE CNF

7 IEEE recommended practice for master/remote supervisory control data acquisition (SCADA) communications

IEEE Std 999-1992 , 12 Feb. 1993

[\[Abstract\]](#) [\[PDF Full-Text \(2628 KB\)\]](#) IEEE STD

8 Effect of diamondlike carbon coating and surface topography on the performance of metal evaporated magnetic tapes

Patton, S.T.; Bhushan, B.;

Magnetics, IEEE Transactions on , Volume: 34 , Issue: 2 , March 1998

Pages:575 - 587

[\[Abstract\]](#) [\[PDF Full-Text \(332 KB\)\]](#) IEEE JNL

9 Environmental effects on the streaming mode performance of metal evaporated and metal particle tapes

Patton, S.T.; Bhushan, B.;

Magnetics, IEEE Transactions on , Volume: 33 , Issue: 4 , July 1997

Pages:2513 - 2530

[\[Abstract\]](#) [\[PDF Full-Text \(396 KB\)\]](#) IEEE JNL

10 High performance concentrators and superconcentrators using multiplexing schemes

Chien, M.V.; Oruc, A.Y.;

Communications, IEEE Transactions on , Volume: 42 , Issue: 11 , Nov. 1994

Pages:3045 - 3050

[\[Abstract\]](#) [\[PDF Full-Text \(640 KB\)\]](#) IEEE JNL

11 Single chip microcomputers and their applications

Murphy, B.;

Solid-State Circuits Conference. Digest of Technical Papers. 1978 IEEE International , Volume: XXI , Feb 1978
Pages:194 - 194

[\[Abstract\]](#) [\[PDF Full-Text \(120 KB\)\]](#) [IEEE CNF](#)

12 PAM-Blox II: design and evaluation of C++ module generation for computing with FPGAs

Mencer, O.;

Field-Programmable Custom Computing Machines, 2002. Proceedings. 10th A IEEE Symposium on , 22-24 April 2002

Pages:67 - 76

[\[Abstract\]](#) [\[PDF Full-Text \(484 KB\)\]](#) [IEEE CNF](#)

13 VLSI system design using asynchronous wave pipelines: a 0.35 μ m CMOS 1.5 GHz elliptic curve public key cryptosystem chip

Hauck, O.; Katoch, A.; Huss, S.A.;

Advanced Research in Asynchronous Circuits and Systems, 2000. (ASYNC 2000 Proceedings. Sixth International Symposium on , 2-6 April 2000

Pages:188 - 197

[\[Abstract\]](#) [\[PDF Full-Text \(792 KB\)\]](#) [IEEE CNF](#)

14 Analog digits: bit level redundancy in a binary multiplier

Saed, A.; Ahmadi, M.; Jullien, G.A.;

Signals, Systems & Computers, 1998. Conference Record of the Thirty-Second Asilomar Conference on , Volume: 1 , 1-4 Nov. 1998

Pages:236 - 240 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(268 KB\)\]](#) [IEEE CNF](#)

15 Visualising reconfigurable libraries for FPGAs

Luk, W.; Guo, S.;

Signals, Systems & Computers, 1997. Conference Record of the Thirty-First Asilomar Conference on , Volume: 1 , 2-5 Nov. 1997

Pages:389 - 393 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(484 KB\)\]](#) [IEEE CNF](#)

[1](#) [2](#) [Next](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved